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IPv6 is an updated version of IPv4 that has been around for a number of years. Use

the Internet and school library to research IPv6, its characteristics, and when it may be fully adopted. Examine the ...

CS401 Operating Systems

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This introduction to the organization and programming of the 8086 family of microprocessors used in IBM microcomputers and compatibles is comprehensive and thorough. Includes coverage of I/O control, video/graphics control, text display, and OS/2. Strong pedagogy with numerous sample programs illustrates practical examples of structured programming.

This widely used, fully updated assembly language book provides basic information for the beginning programmer interested in computer architecture, operating systems, hardware manipulation, and compiler writing.Uses the Intel IA-32 processor family as its base, showing how to program for Windows and DOS. Is written in a clear and straightforward manner for high readability. Includes a companion CD-ROM with all sample programs, and Microsoft® Macro Assembler

Version 8, along with an extensive companion Website maintained by the author. Covers machine architecture, processor architecture, assembly language fundamentals, data transfer, addressing and arithmetic, procedures, conditional processing, integer arithmetic, strings and arrays, structures and macros, 32-bit Windows programming, language interface, disk fundamentals, BIOS-level programming, MS-DOS programming, floating-point programming, and IA-32 instruction encoding.For embedded systems programmers and engineers, communication specialists, game programmers, and graphics programmers.

This book covers elementary discrete mathematics for computer science and engineering. It emphasizes mathematical definitions and proofs as well as applicable methods. Topics include formal logic notation, proof methods; induction, well-ordering; sets, relations; elementary graph theory; integer congruences; asymptotic notation and growth of functions; permutations and combinations, counting principles; discrete probability. Further selected topics may also be covered, such as recursive definition and structural induction; state machines and invariants; recurrences; generating functions.

Professional IT practitioners need not only the appropriate technical skills, but also a broad understanding of the context in which they operate. This book provides a

unique introduction to: social, legal, financial, organizational and ethical issues in the context of the IT industry; the role of professional codes of conduct and ethics; and key legislation. It is designed to accompany the BCS Professional Examination Core Diploma Module: Professional Issues in Information Systems Practice.

This piece covers computer architecture at the instruction set architecture (ISA) and system design levels. Starting with foundation material on data representation and computer arithmetic, the book moves through the basic components of a computer architecture, covering topics at increasing levels of complexity up through CISC, network architecture, and parallel architecture. The authors have adopted the use of a SPARC-subset for an instructional ISA called "ARC" (A RISC Computer), which is carried through the mainstream of the book, and is complemented with platform-independent software tools that simulate the ARC ISA as well as the MIPS and x86 (Pentium) ISAs. FEATURES/BENEFITS Choice of the instruction set architecture (ISA). The mainstream ISA "ARC" is a subset of the commercial SPARC, which strikes a balance between the complexity of a real-world architecture and the need for a simple instructional ISA. Companion Website http: //www.prenhall.com/murdocca Software available on Companion Website. Assembles and simulates program execution on SPARC-subset (ARC), MIPS, and Intel ISAs. Simulators and assemblers run an PCs, Macs, and Unix. Over 400 Adobe

Acrobat slides Simplify lecture preparation. Password-protected area of Companion Website. Case studies. Over 200 homework problems. The major portion of the text deals with a high level look at computer architecture, while the appendices and case studies cover lower level, technology-dependent aspects. Allows computer architecture to be studied at all levels.

By staying current, remaining relevant, and adapting to emerging course needs, Operating System Concepts by Abraham Silberschatz, Peter Baer Galvin and Greg Gagne has defined the operating systems course through nine editions. This second edition of the Essentials version is based on the recent ninth edition of the original text. Operating System Concepts Essentials comprises a subset of chapters of the ninth edition for professors who want a shorter text and do not cover all the topics in the ninth edition. The new second edition of Essentials will be available as an ebook at a very attractive price for students. The ebook will have live links for the bibliography, cross-references between sections and chapters where appropriate, and new chapter review questions. A two-color printed version is also available.

This textbook introduces readers to assembly and its role in computer programming and design. The author concentrates on covering the 8086 family of processors up to and including the Pentium. The focus is on providing students with a firm grasp of the main features of assembly programming, and how it can

be used to improve a computer's performance. All of the main features are covered in depth: stacks, addressing modes, arithmetic, selection and iteration, as well as bit manipulation. Advanced topics include: string processing, macros, interrupts and input/output handling, and interfacing with such higher-level languages as C. The book is based on a successful course given by the author and includes numerous hands-on exercises.