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VIL. Solution Assuming negligible leakage, when $V_{in} < V_T$, transistor M1 is off and $V_{OH} = 2.5V$. For $V_{in} = 2.5V$, assume M1 is in the linear region, and because V_{DS} is negligible in the linear region, channel-length modulation can be ignored. For the linear region, $V_{min} = V_{DS} = V_{out} = V_{OL} = 46.25m$.

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