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Warren Perger earned his BS and MS in Electrical Engineering from the University of Wisconsin-Madison in 1978 and 1981, respectively. He worked for General Telephone for two years, and then he earned ...

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Lei L. Kerr received her Ph.D. in Chemical Engineering from University of Florida in May 2004. She received her B.S. in Chemical

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Engineering from Wuhan Institute of Technology, China in 1997. She ...

~~Lei L. Kerr, Ph.D.~~

Commercial companies, especially electrical and electronics companies, also dominate the three lists, accounting for six of the top ten positions in both the USPTO and JPO lists, and seven in the EP0.

~~Trends in nanotechnology patents~~

```
A);nand N3 (I2, ABbar, B);//The Output
Gatenand N4 ( Out, I1,I2);endmodule// End of
the Sample Structure Module The above code
```

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describes the structural model of an xor gate. This code instantiates the ...

~~Point 3: Structural Modeling Using Verilog~~
High-frequency limitations due to electrical parasitics have been analysed using lumped-element circuit models [[35]]. At high frequencies, (in the GHz range), electrical parasitics cause a roll-off ...

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